



EPT USB FPGA Dev Sys Getting Started Guide

Thanks for purchasing the DueProLogic FPGA Development System.

We have recently upgraded the DueProLogic. This board still offers the same essential features that made it one of the top selling FPGA boards. But, it now hosts a 6x6 LED Array. This LED Array is completely programmable by the user.

- Altera Cyclone IV FPGA
- 54 Inputs/Outputs 3.3V only
- High Speed bi-directional USB data communications with PC
- On Board Flash programming (for configuration of FPGA)
- 36 user LEDs
- Two push buttons
- SD Card connector

If there are any issues with the driver, documentation, FPGA projects or application projects please contact us at:

support@earthpeopletechnology.com

and we will fix whatever problems arise. Or contact us and give us any questions you have about the DueProLogic, your projects, or any electronic question. At Earth People Technology, we are experienced embedded engineers and we live for this stuff. So, happy projecting, we look forward to hearing from you.

EPT Team

How to Disable Driver Signature Verification on 64-Bit Windows 10

Windows 10 implements extra protection against malicious driver files that intend to do harm to the users PC. This implementation unfortunately locks out any third party driver file if it is unsigned. Currently all EPT boards have unsigned drivers. Follow the instructions below to allow Windows 10 to allow third party unsigned drivers to be installed on your PC.



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- Under Windows 10, You must restart the computer in the “options menu” mode. This will allow you to go through the process of allowing all unsigned drivers to self install on your Windows 10.
- First, the computer must be restarted into the so-called “options menu”. The easiest way to get there is via the “Run”-dialog, which is opened by means of the key combination Win+R. The command you have to enter, to boot into the Options menu is as follows:
- shutdown.exe /r /o /f /t 00 Caution: This command starts the reboot process immediately!
- You’ll find the explanation of the individual parameters below:
 - shutdown.exe – It’s a Command-Line application which is inclusive with Windows. It does various kinds of restarts and shutdowns.
 - /r – means “restart”
 - /o – means “the PC should start in the Option menu”
 - /f – means “restart directly and close all opened programs immediately”
 - /t 00 – shows the time until the restart happens (in seconds). In this case 0 seconds, which equals an immediate restart Then click on the Recovery option on the left hand side.
- After you have successfully rebooted into the “Options menu”, click “Troubleshoot” and then “Advanced options”.
- Now click on the “startup settings”-button and the press “reboot”.
- After another reboot, you’re at the startup settings page. Here you can choose between various options, which can be entered by pressing the respective number key. For our needs, you have to click option 7 – “Disable driver signature enforcement”. This deactivates the driver check and enables you to install unsigned drivers in Windows 10.
- In the last step and after another restart, you are able to install unsigned drivers by using Windows “Device Manager”. Don’t get confused. There still will be a question in the beginning, as you can see in the screenshot above, but nevertheless the installation of the driver will be possible without problems.

After these tasks are successfully completed, you can proceed with the following sections.



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Installing Drivers for the EPT USB FPGA Development System

- Plug the USB Micro B cable into the DueProLogic board.
- Plug the other side of the USB cable into an available USB port on your PC.
- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and follow the directions to install the drivers.

Install the Development Software

- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and download and install the Quartus II software
- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and install the EPT_Blaster driver.
- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and download and install the Visual C# 2013 Express software.
- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and install the EPT Active Host driver.