

JTAG DLL Insert to Quartus II

The JTAG DLL Insert to Quartus II allows the Programmer Tool under Quartus to recognize the EPT-570-AP. The EPT-570-AP can then be selected and perform programming of the CPLD. The file, jtag_hw_mbftdi_blaster.dll must be placed into the folder that hosts the jtag_server for Quartus. This dll is available for Windows XP 32-bit, Windows 7 32-bit and Windows 7 64-bit.

Download the EPT_Blaster driver by going to the earthpeopletechnology.com Drivers page. Click on the "Download Now" button.

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At the Browse dialog box, create the directory Earth People Technology\EPT USB-CPLD Development System\Drivers. Save the file under this directory tree.

ganize 👻 New folder			855 -	
Earth People Technology EPT USB-CPLD Development System Discumentation Drivers Projects_ActiveHost_328it Projects_ActiveHost_648it Projects_Arduino	Name Arduino_JDE Documentation Drivers Projects_ActiveHost_32Bit Projects_ActiveHost_64Bit Projects_Arduino	Date modified 2/12/2013 8:21 AM 2/12/2013 8:21 AM 2/12/2013 8:22 AM 2/26/2013 10:40 PM 2/12/2013 8:23 AM 2/12/2013 8:23 AM	Type File folder File folder File folder File folder File folder File folder	
 Projects_HDL Quartus_Programmer File name: RedBoard-v06.zip 	Quartus_Programmer	2/12/2013 8:23 AM	File folder	
Save as type: Al Zin ZID File (* zin)				_

Locate the EPT_Blaster folder in the Drivers folder of the EPT USB-CPLD Development System using Windows Explorer.





If your system is Windows 64 bit follow these directions:

Windows 64 bit

- 1. Open the C:\EPT USB-CPLD Development System\Drivers\EPT_Blaster\x64 folder.
- 2. Select the file "jtag_hw_mbftdi_blaster.dll" and copy it.
- 3. Browse over to C:altera\12.1\quartus\bin64.
- 4. Right click in the folder and select Paste
- 5. Click Ok.
- 6. Open the Quartus II application.

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🐌 ip	*	Name	Date modified	Туре	Size
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🎍 quartus		jam2_ic.jam	11/7/2012 8:39 PM	JAM File	264 KB
鷆 bin	_	jtag_atlantic.dll	11/7/2012 8:39 PM	Application extens	20 KB
🍌 bin64	E	jtag_client.dll	11/7/2012 8:38 PM	Application extens	206 KB
鷆 common		🔞 jtag_hw_mbftdi_blaster.dll	1/16/2013 10:37 PM	Application extens	67 KB
鷆 cusp		jtag_hw_pli-blaster.dll	11/7/2012 8:39 PM	Application extens	33 KB
🁪 drivers		jtag_hw_usb-blaster.dll	11/7/2012 8:39 PM	Application extens	17 KB
🍌 dsp_builder		jtag_hw_virtual_chain.dll	11/7/2012 8:38 PM	Application extens	34 KB
🍌 eda		🚳 jtag_pli-blaster_vpi.dll	11/7/2012 8:39 PM	Application extens	41 KB
🎍 extlibs32		jtagconfig.exe	11/7/2012 8:39 PM	Application	35 KB
🎉 libraries		itagserver.exe	11/7/2012 8:39 PM	Application	269 KB
鷆 Imf		liblzma.dll	11/7/2012 6:57 PM	Application extens	128 KB
鷆 qdesigns		💷 Imgrd.exe	12/8/2005 4:30 PM	Application	1,147 KB
🍌 sopc_builder		2 Intools.exe	12/8/2005 5:33 PM	Application	1,060 KB
👜 altera_inspector.log.zip		💷 Imutil.exe	12/8/2005 4:30 PM	Application	1,113 KB
A 91	*	I man alt fault injection our	11/7/2012 9-42 DM	Application	70 1/12
jtag_hw_mbftdi_blaster.dll Date modifie	d: 1/16/2013 10:3	7 PM Date created: 11/23/2012 11:01 AM			

The DLL is installed and the JTAG server should recognize it. Go to the section "Programming the CPLD" of this manual for testing of the programming. If the driver is not found in the Programmer Tool->Hardware Setup box, see the JTAG DLL Insert to Quartus II Troubleshooting Guide.



If your system is Windows 32 bit follow these directions:

Windows 32 bit

- 1. Open the C:\ EPT USB-CPLD Development System\Drivers\EPT_Blaster\x86 folder.
- 2. Select the file "jtag_hw_mbftdi_blaster.dll" and copy it
- 3. Browse over to C:altera\12.1\quartus\bin.
- 4. Right click in the folder and select Paste
- 5. Click Ok.
- 6. Open the Quartus II application.

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\mu ip	*	Name	Date modified	Туре	Size
ios2eds		🔊 itk34.dll	11/7/2012 6:55 PM	Application extens	43 KB
🐌 quartus		itk34.pdb	3/3/2009 5:16 PM	Program Debug D	187 KB
鷆 bin	_	iam2.jam	11/7/2012 8:26 PM	JAM File	265 KB
鷆 bin64	E	jam2_ic.jam	11/7/2012 8:26 PM	JAM File	264 KB
퉬 common		itag_atlantic.dll	11/7/2012 8:26 PM	Application extens	17 KB
鷆 cusp		itag_client.dll	11/7/2012 8:26 PM	Application extens	131 KB
鷆 drivers		itag_hw_mbftdi_blaster.dll	1/26/2013 4:00 PM	Application extens	60 KB
鷆 dsp_builder		jtag_hw_pli-blaster.dll	11/7/2012 8:26 PM	Application extens	26 KB
鷆 eda		itag_hw_usb-blaster.dll	11/7/2012 8:26 PM	Application extens	15 KB
extlibs32		itag_hw_virtual_chain.dll	11/7/2012 8:26 PM	Application extens	27 KB
鷆 libraries		itag_pli-blaster_vpi.dll	11/7/2012 8:26 PM	Application extens	33 KB
퉬 Imf		itagconfig.exe	11/7/2012 8:26 PM	Application	32 KB
퉬 qdesigns		itagserver.exe	11/7/2012 8:26 PM	Application	192 KB
퉬 sopc_builder		🚳 liblzma.dll	11/7/2012 6:56 PM	Application extens	132 KB
👜 altera_inspector.log.zip		Imgrd.exe	12/1/2010 5:42 PM	Application	1,360 KB
1 91	-	Se Integla ave	10/1/2010 S-42 DM	Application	1 577 //0
jtag_hw_mbftdi_blaster.dll Date modified: 1 Application extension Size: 5	/26/2013 4:00 9.5 KB	PM Date created: 2/10/2013 4:21 P	12/1/2010 5:42 DM	Application	1 577 //D

1.1.1 The DLL is installed and the JTAG server should recognize it. Programming the CPLD

Programming the CPLD is quick and easy. All that is required is a standard USB cable with a Mini Type B connector on one end and the EPT JTAG Driver DLL. Connect the EPT-570-AP to the PC, open up Quartus II, open the programmer tool, and click the Start button. To program the CPLD, follow the steps to install the USB Driver and the JTAG Driver Insert for Quartus II.



Click on the Programmer button.



😲 Quartus II 32-bit - C:/altera/12.0sp1/quartus/qdesigns/e	ot_EPM570_Transfer_Test_top/ept_EPM570_Transfer_Test_top - ept_EPM570_Transfer_Test_top
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Type Message	
 Info: Quartus II 32-bit TimeQues Info (293026): Skipped module Po Info (293000): Quartus II Full C 	t liming Analyzer was successful. 0 errors, 7 warnings werPlay Power Analyzer due to the assignment FLOW_ENABLE_POWER_ANALYZER ompilation was successful. 0 errors, 137 warnings
9 4 III	

The Programmer Window will open up with the programming file selected. Click on the Hardware Setup button in the upper left corner.

ware Setup e real-time ISP to Start Stop	No Hardware allow background programming (for File	MAX II and MAX V de Device	Mode: JTA	G	▼ Pr	ogress:			
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elete	pt_EPM570_Transfer_Test_top.pof CFM UFM	EPM570T100	002D9B91	FFFFFFF					
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The Hardware Setup Window will open. In the "Available hardware items", double click on "EPT-Blaster v1.3b".



Hardware Settings JTAG Se	ettings		
Select a programming hardware hardware setup applies only to	setup to use when prog the current programmer	ramming devices. window.	This programming
Currently selected hardware:	No Hardware		
Available hardware items			
Hardware	Server	Port	Add Hardware
447101-05519-V1-00	Local	MBUSB-U	Remove Hardware
			Class

If you successfully double clicked, the "Currently selected hardware:" dropdown box will show the "EPT-Blaster v1.3b".

Available hardware: Available hardware items Add Hardware Hardware Server Port Add Hardware MBFTD1:Blaster v1.0b Local MBUSB-0 Remove Hardware	elect a programming hardware se ardware setup applies only to the	ngs tup to use when prog current programmer	gramming devices window.	. This programming
Available hardware items Hardware Server Port Add Hardware MBFTD1+Blaster v1.0b Local MBUSB-0 Remove Hardware	Currently selected hardware: 🧃	BFTDI-Blaster v1.0b	[MBUSB-0]	>
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MBFTDI-Blaster v1.0b Local MBUSB-0 Remove Hardw	Hardware	Server	Port	Add Hardware
				Remove Hardware

At this point, Quartus is ready to program the EPT-570-AP. Go to the Uno or Mega User Manual and follow the directions to program the CPLD.